

Specifications

NASA/LaRC has a requirement for a SRAM (Static Random Access Memory) prototype of a space flight qualified part.

The item **shall** meet the defining parameters:

- Industry standard QDRII+ HSTL 1.8V interface
- Device and I/O supply voltages (VDD and VDDQ) of 1.8 V
- 2Mb x 36 (72 Mbit device) QDRII+ SRAM
- Burst length not to exceed 2 words
- 250 MHz Operation
- 165-ball ceramic column grid array
- Military temperature range operation or better (-55°C to +125°C)
- Provided component shall be the prototype of a space flight qualified part
 - The manufacturer shall be certified to manufacture the supplied components using a space-grade QML Class V flow, but the components supplied under this procurement need only be the prototype version of this component.
 - The supplied part shall have the same pinout, device package, footprint, electrical, and memory timing characteristics.
 - The space flight equivalent shall be rated for a total radiation dose of 300 KRads or greater **and** have a latchup immunity of 120 MeV.cm²/mg (at 125°C) or greater.

Qty: 13

Components **shall** be delivered within 13 weeks ARO.