

1553AnalyzerSpecs

Quantity 2 of the following 2 items:

1) MIL-STD-1553 Single Channel Interface Module for 1553 data bus analysis

Standard ExpressCard/54 Form Factor for Laptop compatibility

At least 2 User-Programmable Digital Discrete I/O lines

IRIG-B Time Code Input and Output

Time Tag Clock Input

Time Stamp Capability

Ability to function as a BC (Bus Controller)/Bus Monitor Concurrently or as a Multi-RT (Remote Terminal)/Bus Monitor Concurrently

Ability to emulate up to 31 RT addresses simultaneously

When functioning as a BC (Bus Controller) the ability for streaming and minor/major frame scheduling of messages.

When functioning as a Bus Monitor the ability to filter based on RT address, T/R bit, and sub-addresses

With included software for 1553 software development under windows XP/Vista/7, Linux, and VxWorks operating systems

With included cable assembly for attaching to the 1553 bus and accessing programmable I/O lines

2) windows-based graphical bus analysis/monitor software for use with the interface module listed in (1)

Ability to generate and monitor live 1553 bus data without writing custom application code

Ability to create 1553 messages and frames

Ability to reconstruct 1553 bus activity from stored data with optional message filtering