

COTS FPGA board requirements shall meet the following minimum specifications.

- 3U compact PCI (cPCI) board conforming to PICMG 2.0 R3.0 standard
- cPCI bus speed: 33MHz/66MHz
- cPCI bus signaling: 3.3 volt
- on board FPGA: Xilinx Virtex-6 LX240T, -2 speed, industrial grade
- FPGA - cPCI bus interface: 50 MB sustained at 33 MHz cPCI bus speed
- on board 250 MHz QDR SRAM: 3 independent banks of 2Mx32 each
- FPGA - QDR SRAM bank interface: 32 bit data interface, 200 MHz capable, 160 MHz with vendor provided memory controller
- on board 250 MHz DDR2 SDRAM: 1 bank of 32 Mx32
- FPGA - DDR2 SDRAM interface: 32 bit data interface, 200 MHz capable, 160 MHz with vendor provided memory controller
- on board user defined Flash ROM accessible by the FPGA: 128 Mbit
- on board memory holding one FPGA configuration image and configures the FPGA upon power on.
- on board FMC connector: conform to VITA 57.1 standard
- FPGA - FMC connector interface: conform to VITA 57.1 standard